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APPLICATION NO		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/620,592		07/17/2003	Woo Hyuk Choi	049128-5131	9295	
9629	7590	01/10/2005		EXAMINER		
		& BOCKIUS I	DUDEK, JAMES A			
1111 PENNSYLVANIA AVENUE NW WASHINGTON, DC 20004			**	ART UNIT	PAPER NUMBER	
	·			2871		
				DATE MAILED: 01/10/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

,		Application No.	Applicant(s)			
. Office Action Comments		10/620,592	CHOI ET AL.			
Office A	Action Summary	Examiner	Art Unit			
: 		James A. Dudek	2871			
The MAILIN Period for Reply	G DATE of this communication app	pears on the cover sheet with the c	orrespondence address			
THE MAILING DA - Extensions of time may after SIX (6) MONTHS - If the period for reply sp - If NO period for reply is - Failure to reply within the Any reply received by the	TATUTORY PERIOD FOR REPL'TE OF THIS COMMUNICATION. be available under the provisions of 37 CFR 1.1 from the mailing date of this communication ecified above is less than thirty (30) days, a repl'specified above, the maximum statutory period verset or extended period for reply will, by statute the Office later than three months after the mailing struent. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1) Responsive	to communication(s) filed on					
2a) ☐ This action is		action is non-final.				
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims	S					
4a) Of the above the first term (a) 4a) Of the above term (b) 2-2 6)	is/are rejected is/are objected to are subject to restriction and/o tion is objected to by the Examine s) filed on is/are: a) acc	wn from consideration. r election requirement. er. epted or b) \(\subseteq \) objected to by the B				
Replacement	not request that any objection to the drawing sheet(s) including the correct eclaration is objected to by the Ex	tion is required if the drawing(s) is obj	jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S	.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
·	n's Patent Drawing Review (PTO-948) e Statement(s) (PTO-1449 or PTO/SB/08)	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:				

Art Unit: 2871

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 1-8 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The storage contact hole is confusing since it is not clear what layer encompasses the hole.

Allowable Subject Matter

Claims 1-8 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.

Claims 9-20 are allowed.

The prior art of record teaches a method of fabricating a liquid crystal display device, comprising: forming a gate line on a substrate using a first mask; depositing a first insulation layer, a semiconductor layer, and a data metal layer on the substrate to cover the gate line; forming a semiconductor layer by simultaneously patterning the first insulation layer, the semiconductor layer, and data metal layer using a second mask; depositing a second insulation layer on the substrate to cover the semiconductor layer; forming a first contact hole through the second insulation layer and the semiconductor layer by patterning the second insulation layer using a third mask; and forming a pixel electrode on the second insulation layer to extend across the gate line using a fourth mask. The prior art of record fails to teach in combination with the limitations supra a width of the first contact hole is larger than a width of the gate line.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James A. Dudek whose telephone number is 571-272-2290. The examiner can normally be reached on 9:00-5:30.

Application/Control Number: 10/620,592 Page 3

Art Unit: 2871

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert H. Kim can be reached on 571-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ames A. Dudek Primary Examiner Art Unit 2871